

# 26CN10N-VB TO220 Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT	SUMMARY	
V <sub>(BR)DSS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
100	0.017 at V <sub>GS</sub> = 10 V	70 <sup>a</sup>

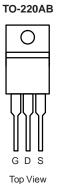
#### FEATURES

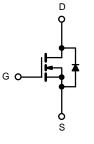
- TrenchFET<sup>®</sup> Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R<sub>g</sub> Tested

#### **APPLICATIONS**

• Isolated DC/DC Converters







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T <sub>C</sub> = 25 °C, unless oth	erwise noted		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current ( $T_{1}$ = 175 °C)	T <sub>C</sub> = 25 °C	1-	70 <sup>a</sup>	
Continuous Drain Current (1) = 175 C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	35 <sup>a</sup>	A
Pulsed Drain Current		I <sub>DM</sub>	145	A
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	31	
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.11111	E <sub>AS</sub>	60	mJ
	T <sub>C</sub> = 25 °C	P	355 <sup>c</sup>	
Maximum Power Dissipation <sup>b</sup>	T <sub>A</sub> = 25 °C <sup>d</sup>	- P <sub>D</sub> -	3.35	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE F	ATINGS			
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount	R <sub>thJA</sub>	40	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.4	0/11

Notes:

a. Package limited.

b. Duty cycle  $\leq$  1 %.

c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

<b>SPECIFICATIONS</b> $T_J = 25^{\circ}$	C, unless o	therwise noted					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			-	1			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 V, I_{D} = 250 \mu A$	100			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			50	μA	
		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	120			А	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.017			
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.023		Ω	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.037			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			1800			
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		210		Ω	pF
Reverse Transfer Capacitance	C <sub>rss</sub>			110			
Total Gate Charge <sup>c</sup>	Qg			90			
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 100 V, $V_{GS}$ = 10 V, $I_{D}$ = 58 A		23		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			34			
Gate Resistance	R <sub>g</sub>		0.5	1.3	3.1	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			24	35		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 100 V, R <sub>L</sub> = 1.5 $\Omega$		220	330	nC	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 58$ A, $V_{GEN}$ = 10 V, $R_g$ = 2.5		45	70		
Fall Time <sup>c</sup>	t <sub>f</sub>	Ω		200	300		
Source-Drain Diode Ratings and Cha	aracteristics 7	<sub>C</sub> = 25 °C <sup>b</sup>		•			
Continuous Current	ا <sub>S</sub>				70		
Pulsed Current	I <sub>SM</sub>			115		A	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 58 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			130	200	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs		8	12	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	· · ·		0.52	1.2	μC	

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

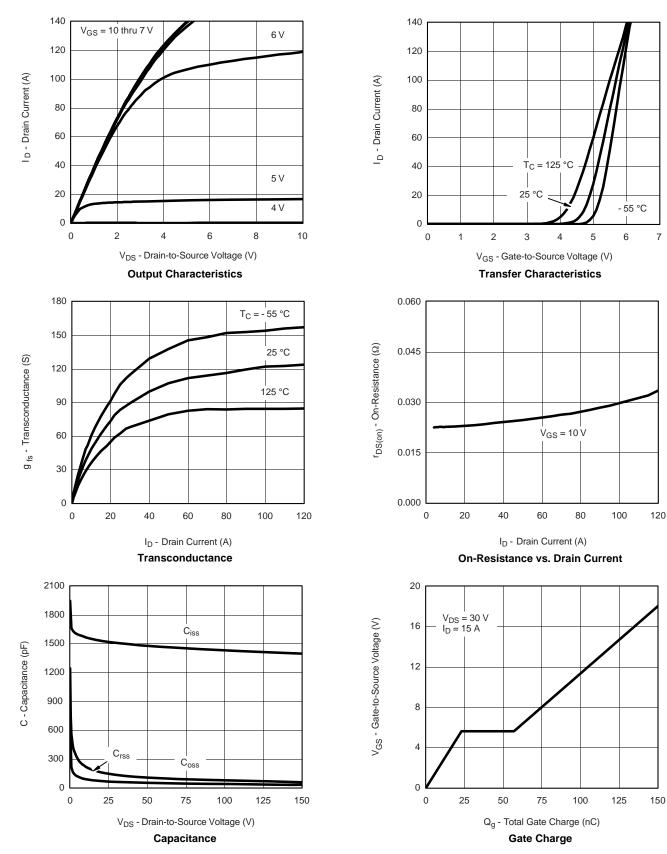
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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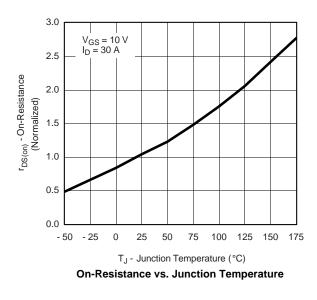


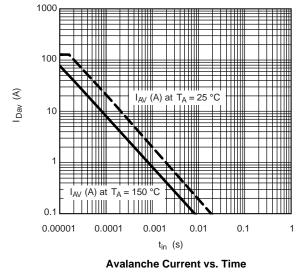
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

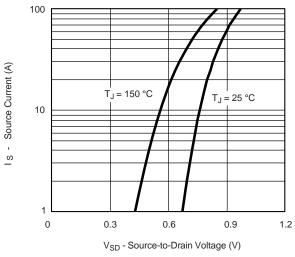




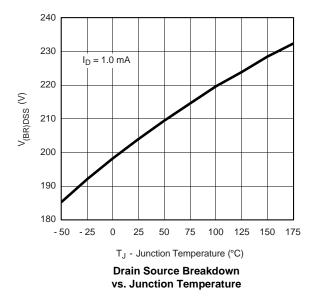
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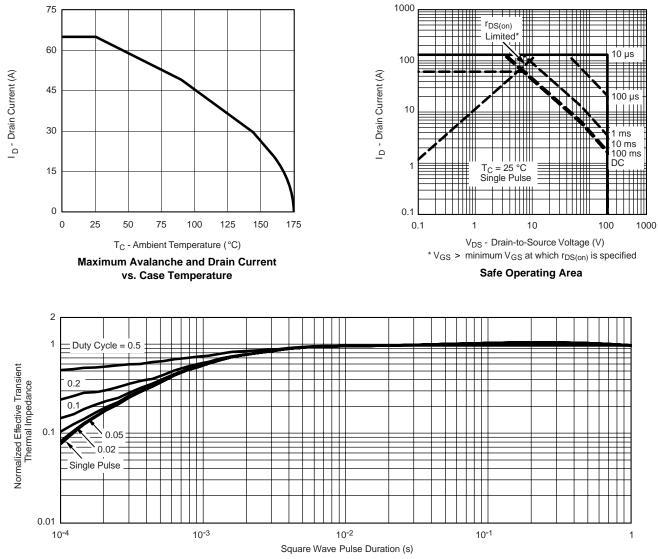
Source-Drain Diode Forward Voltage



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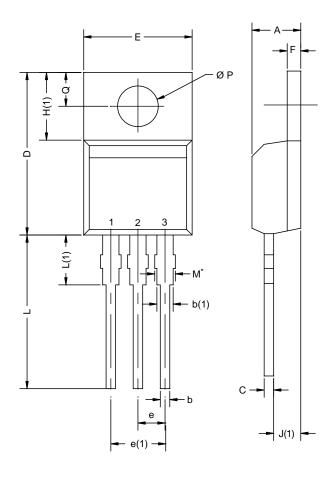
#### **THERMAL RATINGS**



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220AB**



DIM.	MILLIN	MILLIMETERS		HES
	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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